

Yusuf Leblebici

Yusuf Leblebici received the B.Sc. and M.Sc. degrees in electrical engineering from Istanbul Technical University, Istanbul, Turkey, in 1984 and 1986, respectively, and the Ph.D. degree in electrical and computer engineering from the University of Illinois, Urbana-Champaign (UIUC), IL, USA, in 1990. Between 1991 and 2000, he worked as a faculty member at UIUC, at Istanbul Technical University, and at Worcester Polytechnic Institute (WPI) where he established and directed the VLSI Design Laboratory. From 1999 to 2002, he also served as the Microelectronics Program Coordinator at Sabanci University.

Since 2002, he is a Chair Professor at the Swiss Federal Institute of Technology in Lausanne (EPFL) and Director of the Microelectronic Systems Laboratory. His research interests include design of high-speed CMOS digital and mixed-signal integrated circuits, computer-aided design of VLSI systems, intelligent sensor interfaces, modeling and simulation of semiconductor devices, and VLSI reliability analysis. He is the coauthor of seven books (including two internationally acclaimed textbooks), as well as more than 250 articles published in various journals and conferences.

Yusuf Leblebici has been the recipient of the prestigious Young Scientist Award of the Turkish Scientific and Technological Research Council in 1995, and the "Joseph S. Satin Distinguished Fellow Award" in 1999. He was elected as an IEEE Fellow in 2009, and as a Distinguished Lecturer of the IEEE Circuits and Systems Society for 2010–2011.